

HSM122

Silicon Epitaxial Planar Diode for High Voltage Switching

REJ03G1299-0100 Rev.1.00 Oct 27, 2005

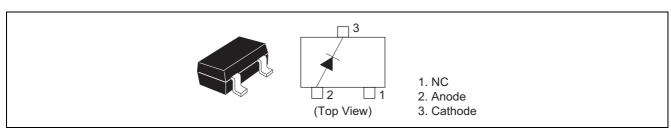
Features

- High reverse voltage. $(V_R = 400 \text{ V})$
- MPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code (Previous Code)
HSM122	F8	MPAK	PLSP0003ZC-A (MPAK)

Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Peak reverse voltage	V_{RM}	420	V
Reverse voltage	V _R	400	V
Peak forward current	I _{FM}	625	mA
Non-Repetitive peak forward surge current	I _{FSM} *1	1	Α
Average rectified current	Io	150	mA
Power dissipation	Pd * ²	150	mW
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Notes: 1. Value at duration of 1s.

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V_{F}		_	1.2	>	I _F = 100 mA
Reverse current	I _R	_	_	1.0	μΑ	V _R = 400 V
Capacitance	С	_	_	10	pF	V _R = 0 V, f = 1 MHz
Reverse recovery time	t _{rr}		_	20	μs	I_F = 30 mA, V_R = 10 V, R_L = 2 k Ω

^{2.} Standard substrate mounting (20mm \times 15mm \times 0.8t mm, With Polyimide board)

Main Characteristic

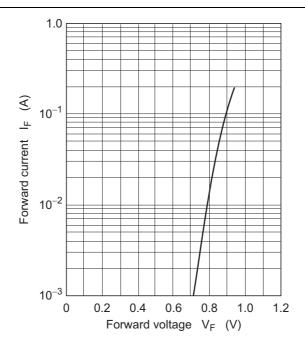


Fig.1 Forward current vs. Forward voltage

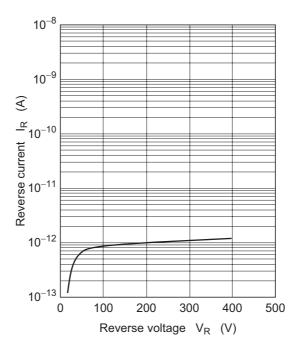
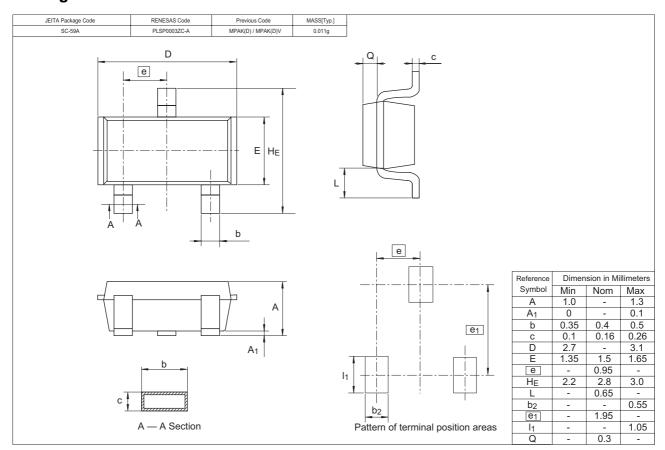


Fig.2 Reverse current vs. Reverse voltage

Package Dimensions



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